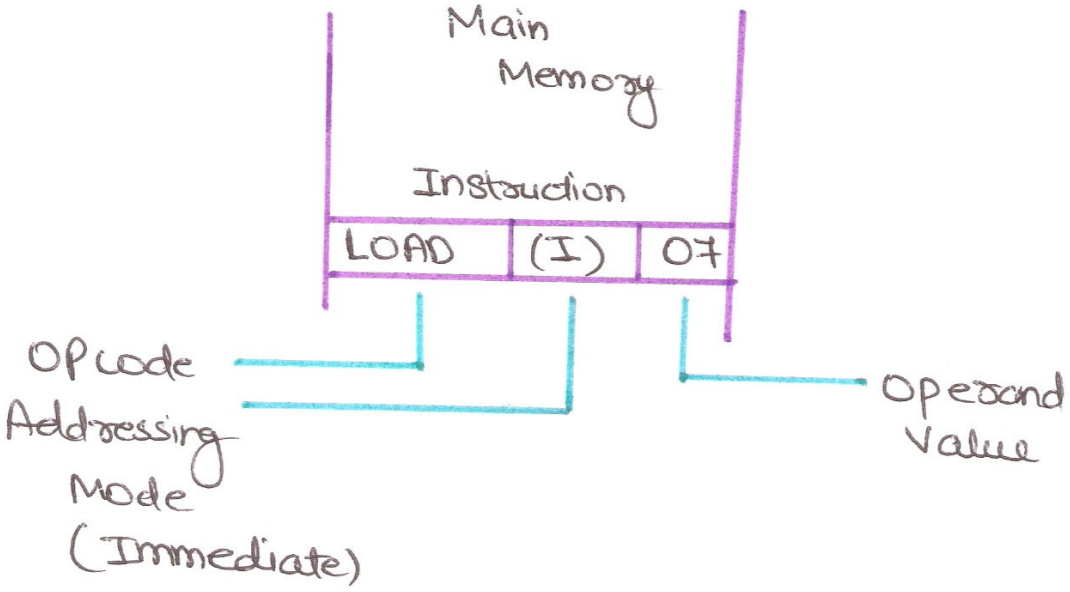
Addressing Modes :- Immediate Addressing

When an operand is interpreted as an 'Immediate' Value

eg LOAD IMMEDIATE 7,

In this the actual value 7 that has to be put in CPU Register.

In this mode the operand is the Data in Operand Addressing field of the instruction. Since there is no address field at all, and hence no additional memory accesses are required for executing this instruction.



In other words, the actual operand 'D' is 'A', the Content of the operand field i.e D=A. The effective Address is not defined

→ In this no additional memory Accesses are required for executing the instruction.

→ The size of instruction and operand field is limited. Therefore the type of data specified under this addressing scheme is also restricted.

If an instruction of 16 bits uses 6 bits for opcode and 2 bits for addressing mode, then 10 bits can be used to specify an operand. Thus 2^{10} possible values only can be assigned.